

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : **Minari ARAI**  
Filed : **Concurrently herewith**  
For : **SEMICONDUCTOR INTEGRATED....**  
Serial No. : **Concurrently herewith**

December 5, 2003


Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRIORITY CLAIM AND**  
**SUBMISSION OF PRIORITY DOCUMENT**

S I R:

Applicant hereby claims priority under 35 USC 119 from **Japanese** patent application number **2002-354993** filed **December 6, 2002**, a copy of which will follow.

Respectfully submitted,

  
\_\_\_\_\_  
Michael I. Markowitz  
Reg. No. 30,659

Katten Muchin Zavis Rosenman  
575 Madison Avenue  
New York, NY 10022-2585  
(212) 940-8800  
Docket No.: NECE 20.780